

Nagarajan Palavesam

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RESEARCH EXPERIENCE

Fraunhofer EMFT, Munich, Germany

2013 August – Present

www.emft.fraunhofer.de

Research Associate, Polytronics and Multifunctional Systems

Development of new packaging and integration technologies for components on flexible and stretchable film substrates and reliability testing

University of Geneva, Geneva, Switzerland

2012 August – 2013 May

www.unige.ch

Research Assistant, Dept. of Condensed Matter Physics

Development of a novel Scanning Tunneling Microscope with micro fabricated non-magnetic tips to study atomic-scale spin contrast

ABB Corporate Research Center, Baden, Switzerland

2011 September – 2012 March

www.abb.ch

Research Intern, Sensor technologies group

Development of a Tunable Diode Laser Absorption Spectroscopy based sensor to measure the concentration of Ammonia in a gaseous mixture

IBM Almaden Research Center, San Jose, USA

2011 March – 2011 August

www.almaden.ibm.com

Research Intern and Master Thesis student, Nanofabrication group

Onsite advisor: Dr. Luisa D. Bozano

Academic advisors: Prof. Fabrizio Pirri/Prof. Fabrizio Giorgis

Analysis of the Conductive Discharge Layer (Polyaniline based) to reduce Image Placement errors in Electron - beam lithography

Korea University, Seoul, South Korea

2010 July – 2010 August

www.nanodev.korea.ac.kr

Research Intern, Nanodevices lab

Fabrication of Nano-electronic devices using Zinc Oxide nanowires by selective patterning with Electron-beam lithography and Electrical Characterization

EDUCATION

Swiss Federal Institute of Technology, Lausanne, Switzerland/Grenoble Institute of Technology, Grenoble, France/Polytechnic University of Turin, Turin, Italy

2009 September – 2012 March

www.master-nanotech.com

Master of Science, Micro and Nanotechnologies for Integrated Systems

Anna University, Chennai, India

2004 October – 2008 April

www.annauniv.edu

Bachelor of Engineering, Electrical and Electronics Engineering

AWARDS AND HONOURS

Marie Curie Fellowship

Awarding body: European Commission

2014 – 2016

Marie Curie Early Stage Research fellowship to work on the ITN project, “CONTEST”

Alta Scuola Politecnica Scholarship

2009 – 2010

Awarding body: Polytechnic University of Turin, Turin, Italy and Polytechnic University of Milan, Milan, Italy

Merit based scholarship covering Master’s degree tuition fee for the academic year and living allowance

Compagnia di San Paolo Scholarship

2010 – 2011

Awarding body: Compagnia di San Paolo, Turin, Italy

Merit based scholarship covering Master’s degree tuition fee for the academic year and living allowance

EDISU Piemonte International Mobility Scholarship

2010 – 2011

Awarding body: EDISU Piemonte, Turin, Italy

Merit based supplementary scholarship covering travel and living expenses for studies and internship abroad

PUBLICATION

Luisa D. Bozano, Ratnam Sooriyakumaran, Linda K. Sundberg, Martha I. Sanchez, Elizabeth M. Lofano, Charles T. Rettner, Takayuki Nagasawa, Satoshi Watanabe, Yoshio Kawai, Nagarajan Palavesam and Gustavo Gandara Montano, “**Conductive layer for charge dissipation during electron-beam exposures**”, Proc. SPIE 8325, Advances in Resist Materials and Processing Technology XXIX, 83250X (March 29, 2012), doi:10.1117/12.916756

RESEARCH INTERESTS

Integration technologies for flexible electronic systems, New materials for flexible electronics and integration, Micro and Nano fabrication, Micro and Nano Electronics, Flexible and Organic Electronics, Micro and Nano characterization, Interdisciplinary research

LANGUAGE SKILLS

Native or bilingual proficiency

English

IELTS – 8.0/9.0

Tamil

Limited working proficiency

Italian

Polytechnic University of Turin, Turin, Italy

French

Alliance française, Grenoble, France

Elementary proficiency

German

Goethe-Institut, Chennai, India